

NAME: M.N. KHAN

ID: 2216080041

COURSE: CSE-213 - Digital Logic Design

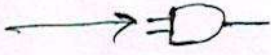
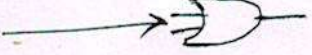

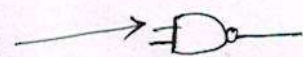

8th Batch

CSE

Ans: to: the: Q: NO: 01

(a) Ans: Defining DLD:- Digital Logic is the basic of electronic system - such as computers & cell phones. Digital logic is noted in binary code, a series of zeros & one each having an opposite value. The system facilitates the design of electronic circuits that convey information including logic gate function include & and or and not. The value system translate input signals into specific Application.

Q: DLD list the fields:

- ① AND 
- ② OR 
- ③ NOT 
- ④ NAND 
- ⑤ XOR 

⇒ DLD list Field: Digital logic Design is foundational to the fields of electrical engineering & computers

1a

engineering. Digital logic designing Build
Complex electronic components that use both
electronic & computational characteristics. These
characteristics may involve power, current,
logical functions, protocol & user input. Digital
logic Design is used to develop hardware, such as
circuit boards & microchip processor. This
hardware processor user input, system protocol
& other data in Computers, Navigational systems,
cell phones or other high-tech system.

— X —

1/(b) Advantage of DLD: The Advantage of using a ROM in this way is that any conceivable function of the m inputs can be made to appear at any of the Output. Making this general purpose. combinatorial logic device available.

→ High Accuracy & programmability.

→ Storage of Digital data is easy.

→ Immune to noise.

→ can be implemented in the form of integrated circuits.

→ Greater reliability & flexibility.

→ They are usually much slower than dedicated logic circuits.

→ They consume more.

x

Ans: to the: of: No: 02

Ex: Converting the number:

(a) Ans: $(715)_{10} = (?)_8$

$= (1295)_8$
Ans

$$\begin{array}{r} 8 \overline{) 715} \\ \underline{89} \\ 8 \overline{) 10} \\ \underline{8} \\ 2 \\ 8 \overline{) 2} \\ \underline{0} \\ 0 \end{array} \uparrow$$

$= 1295$

(b) Ans: $(AC09)_{16} = (?)_{10}$

We know,

$A = 10$

$B = 11$

$C = 13$

$= (A \times 16) + (C \times 16) + (0 \times 16) + (9 \times 16)$

$= 10 \times 16^3 + 13 \times 16^2 + 0 \times 16^1 + 9 \times 16^0$

$= 10 \times 4096 + 13 \times 256 + 0 + 9 \times 1$

$= 40960 + 3328 + 9$

$= 44297$

$\Rightarrow (AC09)_{16} = (44297)_{10}$ Ans

$$\frac{2}{(c)} \text{ Ans: } (100011)_2 = (?)_{10}$$

$$= 1 \times 2^5 + 0 \times 2^4 + 0 \times 2^3 + 0 \times 2^2 + 1 \times 2^1 + 1 \times 2^0$$

$$= 1 \times 32 + 0 + 0 + 0 + 2 + 1$$

$$= 32 + 2 + 1$$

$$= 35$$

$$\Rightarrow (100011)_2 = (35)_{10} \text{ Ans:}$$

$$(d) \text{ Ans: } (12435)_8 = (?)_{10}$$

$$= 1 \times 8^4 + 2 \times 8^3 + 4 \times 8^2 + 3 \times 8^1 + 5 \times 8^0$$

$$= 4096 + 1024 + 256 + 24 + 5$$

$$= 5405$$

$$\Rightarrow (12435)_8 = (5405)_{10} \text{ Ans:}$$

(a) Ans: Defining the example of LSB & MSB -

MSB: MSB stands for most significant bit, while LSB is least significant bit. In binary terms, the MSB is the bit that has the greatest effect on the number & it is the left-most bit.

For example - For a binary number 00110101, the most significant 4 bits would be 0011. The least significant 4 bit would be 0101.

LSB: Least - significant bit. In a binary number the LSB is the least weighted bit in the number.

Parameter	eCMSB	eLSB	Value
Gain	16	48	
Low	17	49	
Mid freq	18	50	
Mid	19	51	
high	20	52	
level	21	53	
Expression	4	36	
" Mid	4	33	

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$$(iv) (AB9EF)_{16} = (?)_{10}$$

$$\begin{aligned} &= A \times 16 + B \times 16 + 9 \times 16 + E \times 16 + F \times 16 \\ &= 10 \times 16^4 + 11 \times 16^3 + 9 \times 16^2 + 14 \times 16^1 + 15 \times 16^0 \\ &= 655360 + 45056 + 2304 + 224 + 15 \\ &= 702959 \end{aligned}$$

$$\Rightarrow (AB9EF)_{16} = (702959)_{10}$$

Ans!

we know that

$$A = 10$$

$$B = 11$$

$$C = 12$$

$$D = 13$$

$$E = 14$$

$$F = 15$$