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Department of Computer Science & Engineering

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Course Title: Digital Logic Design.


①

Answer to the question no: 1 (a)

1. (a) Ans: Defining DLD: Digital logic is the basic of electronics system such as computers & cell phones. Digital logic noted in binary code, a series of zeros & one each having an opposite value. The system facilitates the design of electronic circuits that convey information including logic gate. Function include and, or, and not. The value system transtate input signals into specific Application.

#DLD list the fields:

① AND $\longrightarrow \Rightarrow$ 

② OR $\longrightarrow \Rightarrow$ 

③ NOT $\longrightarrow \Rightarrow$ 

④ NAND $\longrightarrow \Rightarrow$ 

⑤ XOR $\longrightarrow \Rightarrow$ 

P.T.O.

(2)

#DLD List field: Digital logic design is functional to the fields of electrical engineering & computer engineering. Digital logic design build complex electronics computers that use both electrical & computational characteristics. These characteristics may involve power, current logical functions, protocol & user input. Digital logic design is used to develop hardware, such as circuit boards & microchip processor. This hardware processor user input, system protocol & other data in computers navigational system, cell phones, or other high-tech system.

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Answer to the question no: 1(b)

1. (b) Ans: Advantage of DLD: The advantage of using a ROM is this way, is that any conceivable function of the inputs can be made to appear at any of the output. Making them general purpose. Combinational logic device available.

High accuracy & programmability.

Storage of digital data is easy.

Immune to noise.

Can be implemented in the form of integrated circuits.

Greater reliability & flexibility.

They are usually much slower than dedicated logic circuits.

They consume more power.

(1)

Answer to the question no: 2(a)

2.(a) Ans: Converting the number:

$$(715)_{10} = (?)_8$$

$$= (1295)_8 \quad \underline{\text{Ans:}}$$

$$\begin{array}{r} 8 \overline{) 715} \\ \underline{89-5} \\ 8 \overline{) 10-9} \\ \underline{1-2} \\ 0-1 \\ \hline = 1295 \end{array}$$

2.(b) Ans: $(AC09)_{16} = (?)_{10}$

We know,

$$A = 10$$

$$B = 11$$

$$C = 12$$

$$= (A \times 16^3) + (C \times 16^2) + (0 \times 16^1) + (9 \times 16^0)$$

$$= 10 \times 16^3 + 12 \times 16^2 + 0 \times 16^1 + 9 \times 16^0$$

$$= 10 \times 4096 + 12 \times 256 + 0 + 9 \times 1$$

$$= 40960 + 3072 + 9$$

$$= 44297$$

$$\Rightarrow (AC09)_{16} = (44297)_{10}$$

Ans:

⑤

2. (c) Ans: $(100011)_2 \rightarrow (?)_{10}$

$$= 1 \times 2^5 + 0 \times 2^4 + 0 \times 2^3 + 0 \times 2^2 + 1 \times 2^1 + 1 \times 2^0$$

$$= 1 \times 32 + 0 + 0 + 0 + 2 + 1$$

$$= 32 + 2 + 1$$

$$= 35$$

$\Rightarrow (100011)_2 = (35)_{10}$ Ans:

2. (d) Ans: $(12435)_8 \rightarrow (?)_{10}$

$$= 1 \times 8^4 + 2 \times 8^3 + 4 \times 8^2 + 3 \times 8^1 + 5 \times 8^0$$

$$= 4096 + 1024 + 256 + 24 + 5$$

$$= 5405$$

$\Rightarrow (12435)_8 = (5405)_{10}$

Ans:

Answer to the question no: 3(a)

3. (a) Ans: Defining the example of LSB & MSB—

#MSB: MSB stands for most significant bit while LSB is least significant bit. In binary terms. The MSB is the 8 bit that has the greatest effect on the number & it is the left-most bit.

#For Example: For a binary number 00110101. The most significant 4 bits would be 0011. The most significant 4 bit would be 0101.

#LSB: Least-significant bit of a binary number the LSB is the least weighted bit in the number.

Parameter	CMSB	CLSB	Values
Gain	16	48	
Low	17	49	
Mid Prog	18	50	
Mid	19	51	
high	20	52	
level	21	53	
Expression	4	36	
" Mid	•	33	

(8)

3. (b) (iv) $(AB9EF)_{16} = (?)_8$

$$= A \times 16 + B \times 16 + 9 \times 16 + E \times 16 + F \times 16$$

$$= 10 \times 16^4 + 11 \times 16^3 + 9 \times 16^2 + 14 \times 16^1 + 15 \times 16^0$$

$$= 655360 + 45056 + 2304 + 224 + 15$$

$$= 702959$$

$$\Rightarrow (AB9EF)_{16} = (702959)_8$$

Answer

We know that

$$A = 10$$

$$B = 11$$

$$C = 12$$

$$D = 13$$

$$E = 14$$

$$F = 15$$